

9 7 INPUT/OUTPUT CHANNEL AUXILIARY STORAGE DEVICE MAIN STORAGE DEVICE FIG.2 CPU COMMAND INTERRUPT INPUT DEVICE

ENCORE OF THE CONTROL OF THE CONTROL





FIG.3
TABLE TOP ITEMS (CONTROL POINT 1)

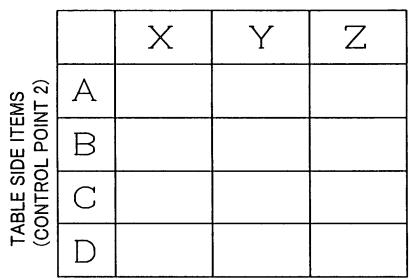


FIG.4

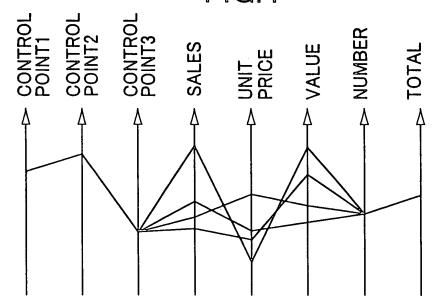


FIG.5

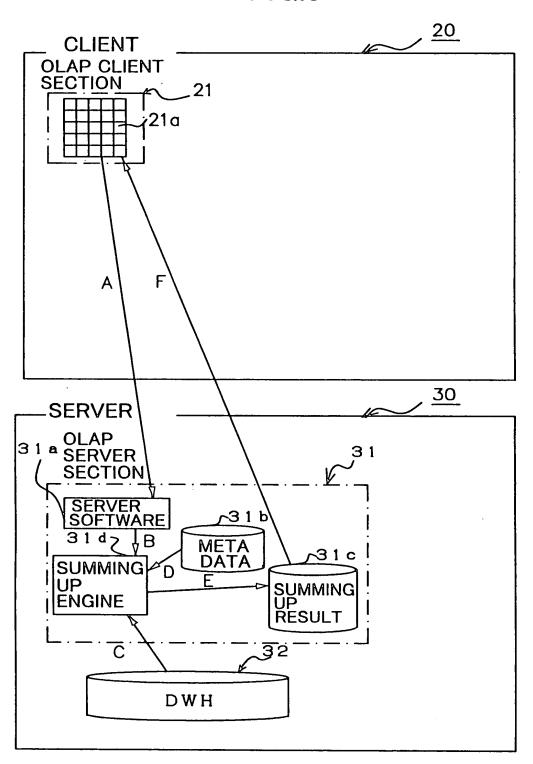
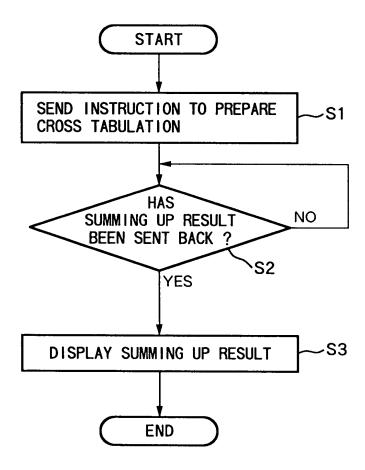
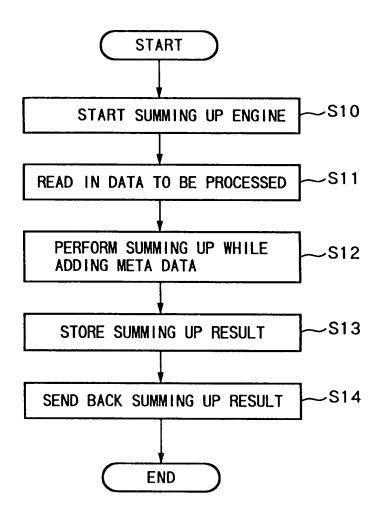


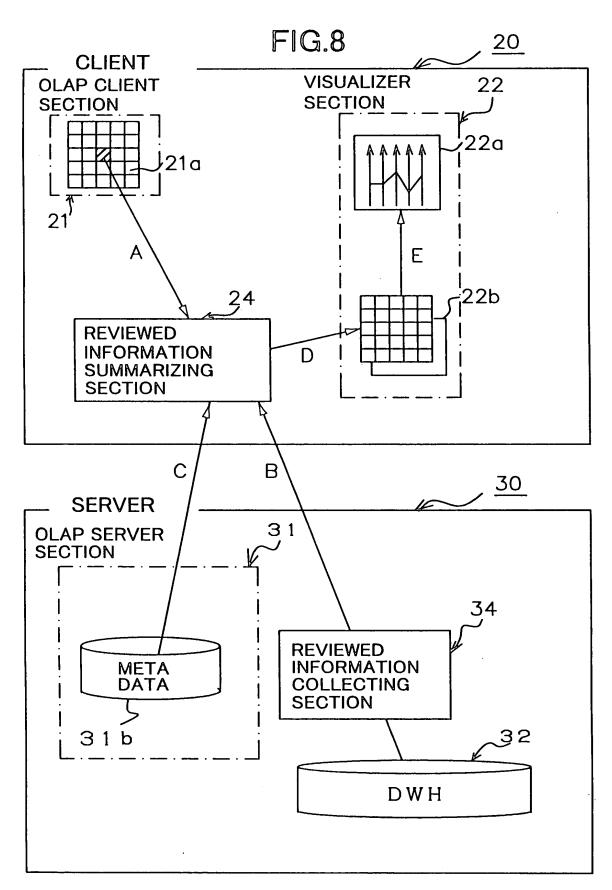
FIG.6



THE BUT OF SOME PORT OF STREET PORTS OF STREET

FIG.7





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FIG.9

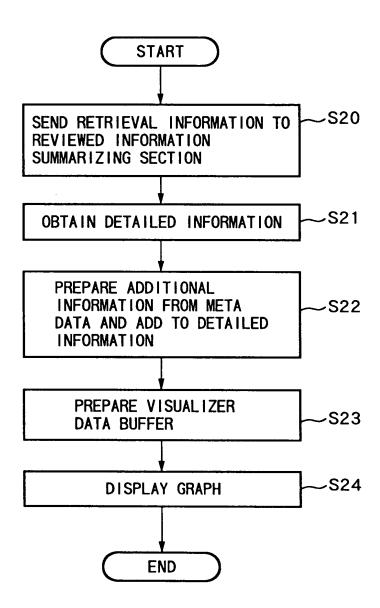
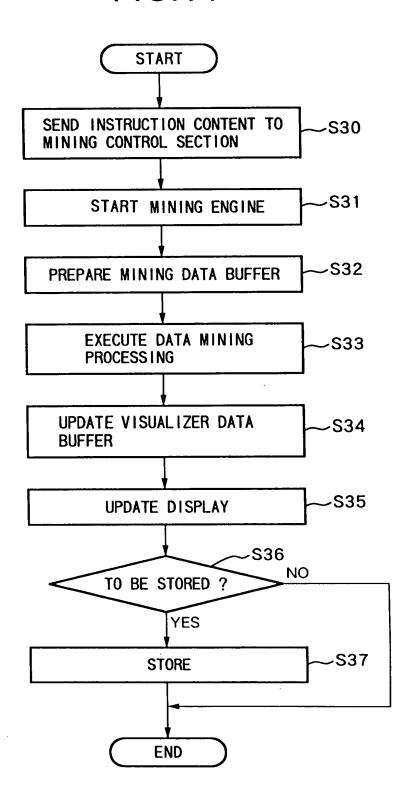


FIG.11



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the line and the



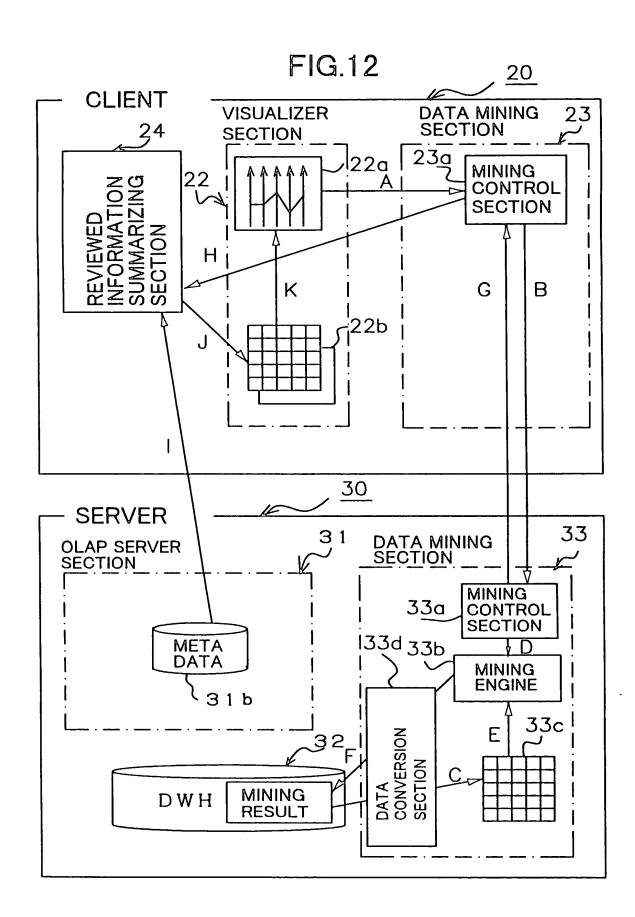


FIG.13

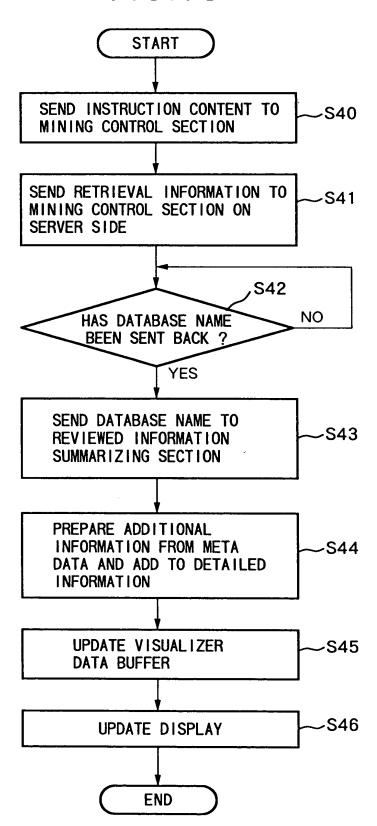


FIG.14

